

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A liquid crystal display, comprising:
 - a substrate;
 - a gate line formed on the substrate and extending in a first direction, wherein the gate line is bent at a positive or negative angle with respect to the direction of a rubbing direction on the substrate;
 - a data line intersecting the first direction;
 - a first pixel electrode formed in a pixel area defined by intersections of the gate line and the data line, said first pixel electrode formed substantially parallel to the gate line;
 - a pixel signal line connected to the pixel electrode;
 - a switching element connected to the gate line, the data line, and the pixel signal line;
 - a first common electrode formed in the pixel area parallel to said first pixel electrode;
 - a common signal line formed in the pixel area connected to said common electrode, wherein the distance between the common signal line and the data line is shorter than the distance between the pixel signal line and the data line;

a first capacitor electrode formed in the pixel area connected to the pixel signal line;

a second capacitor electrode formed in the pixel area connected to said common signal line;

a second pixel electrode formed in the pixel area opposite to the first pixel electrode and connected to the pixel signal line; and

a second common electrode formed in the pixel area, said second common electrode opposite to the first common electrode and connected to the common signal line,

wherein at least the first or second capacitor electrode is triangular in shape.

2. (Original) The liquid crystal display of claim 1, wherein the pixel signal line overlaps the common signal line.

3. (Previously Amended) The liquid crystal display of claim 1, wherein the common signal line is parallel to the data line.

4. (Canceled)

5. (Previously Amended) The liquid crystal display of claim 1, wherein the gate line bends at a positive or negative angle with respect to the perpendicular direction of the data line.

6. - 8. (Canceled)

9. (Previously Amended) The liquid crystal display of claim 1, wherein the first common electrode is disposed nearer to the gate line than the first pixel electrode.

10. (Previously Amended) The liquid crystal display of claim 1, wherein the second common electrode is disposed nearer to the gate line than the second pixel electrode.

11. (Currently Amended) The liquid crystal display of claim 1, wherein further comprises a plurality of pixel areas are provided disposed along the direction of the gate line.

12. (Currently Amended) The liquid crystal display of claim 1, wherein further comprises a plurality of pixel areas are provided disposed symmetrically with respect to the data line therebetween.

13. (Previously Amended) The liquid crystal display of claim 1, 11 or 12, wherein the pixel area is triangular in shape.

14. (Original) The liquid crystal display of claim 1, wherein the pixel electrode and the common electrode are disposed on the same planar plane.

15. (Previously Amended) The liquid crystal display of claim 1 or 14, wherein the pixel electrode and the common electrode have a thickness of less than about 2000 Å.

16. (Original) The liquid crystal display of claim 1, wherein the capacitor electrodes are disposed in a longitudinal center of the pixel area.

17. (Original) The liquid crystal display of claim 1, wherein the first capacitor electrode is a part of the first pixel electrode.

18. (Original) The liquid crystal display of claim 1, wherein the pixel area has a rectangular shape.

19. (Previously Amended) The liquid crystal display of claim 1, wherein the gate line is formed of at least one material selected from a group of Al, Al-alloy, Ag, Ag-alloy and its alloy.

20. (Previously Amended) The liquid crystal display of claim 19, wherein the gate line further comprises a pad layer.

21.-37. (Withdrawn)

38. (Currently Amended) A liquid crystal display, comprising:
a substrate;

a gate line formed on the substrate and extending in a first direction;
a data line intersecting the first direction;
a first pixel electrode formed in a pixel area defined by intersections of the gate line and the data line, said first pixel electrode formed substantially parallel to the gate line;
a pixel signal line connected to the pixel electrode;
a switching element connected to the gate line, the data line, and the pixel signal line;
a first common electrode formed in the pixel area parallel to said first pixel electrode;
a common signal line formed in the pixel area connected to said common electrode;
~~a first capacitor electrode formed in the pixel area connected to the pixel signal line;~~
~~a second capacitor electrode formed in the pixel area connected to said common signal line;~~
a second pixel electrode formed in the pixel area opposite to the first pixel electrode and connected to the pixel signal line; and
a second common electrode formed in the pixel area, said second common electrode opposite to the first common electrode and connected to the common signal line,

wherein an outermost electrode of the first pixel electrode, the pixel signal line and an outermost electrode of the second pixel electrode join together to form a trapezoid shape.

~~wherein the pixel signal line overlaps the common signal line.~~

39. (New) The liquid crystal display of claim 1, wherein a plurality of pixel areas are provided disposed point-symmetrically with respect to the gate line therebetween.